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| Instruction | Opcode (4 bits) [15-12] | Rs (2 bits)  [11-10] | Rt (2 bits)  [9-8] | Rd (2 bits)  [7-6] | Unused (6 bits)  [5-0] | Binary Instruction |
| and $t3, $t1, $t2 ($t3= 7) | 0000 | 01 | 10 | 11 | 000000 | 0000011011000000 |
| sub $t2, $t1, $t3 ($t2= 8) | 0001 | 01 | 11 | 10 | 000000 | 0001011110000000 |
| or $t2, $t2, $t3 ($t2=15) | 0011 | 10 | 11 | 10 | 000000 | 0011101110000000 |
| add $t3, $t2, $t3 ($t3=22) | 0010 | 10 | 11 | 11 | 000000 | 0010101111000000 |
| slt $t1, $t3, $t2 ($t1= 0) | 0100 | 11 | 10 | 01 | 000000 | 0100111001000000 |
| slt $t1, $t2, $t3 ($t1= 1) | 0100 | 10 | 11 | 01 | 000000 | 0100101101000000 |

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| Instruction | Opcode (4 bits) [15-12] | Rs (2 bits)  [11-10] | Rt (2 bits)  [9-8] | Value or Address (8 bits)  [7-0] | Binary Instruction |
| addi $t1, $0, 15 ($t1=15) | 0111 | 00 | 01 | 00001111 | 0111000100001111 |
| addi $t2, $0, 7 ($t2= 7) | 0111 | 00 | 10 | 00000111 | 0111001000000111 |

Shape

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